

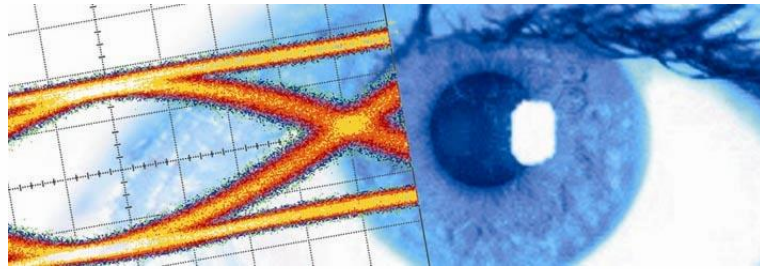


## SHF Communication Technologies AG

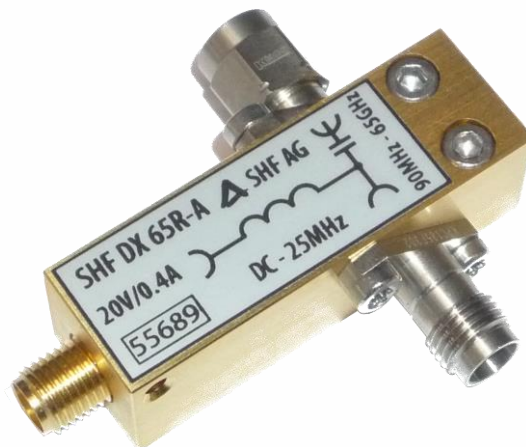
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# Datasheet SHF DX65R Diplexer





## Description

The SHF DX65R diplexer is the RoHS compliant successor of the SHF DX65. A diplexer is essentially a bias tee with a certain bandwidth in the low frequency path to combine or separate high frequency and low frequency signals into or from a single line.

Any existing DC content is blocked from its HF input. Based on SHF's air line construction, it offers resonance-free transmission up to 65 GHz. In addition to the low insertion loss, all products have an extremely low group delay ripple.

## Applications

- Optical Communications
- High-Speed Pulse Experiments
- Satellite Communications
- Research and Development
- Antenna Measurements
- Data Transmission

## Configurations

- A - HF port: 1.85 mm male, HF+LF port: 1.85 mm female
- B - HF port: 1.85 mm female, HF+LF port: 1.85 mm male
- C - HF port: 1.85 mm male, HF+LF port: 1.85 mm male
- D - HF port: 1.85 mm female, HF+LF port: 1.85 mm female

One of above configurations has to be chosen. For more information, please be referred to the mechanical drawing on the last page of this data sheet. The low frequency port is always SMA female.

## Options

- HV100 - High Voltage (maximum voltage extended to 100 V)



## Specifications - SHF DX65R

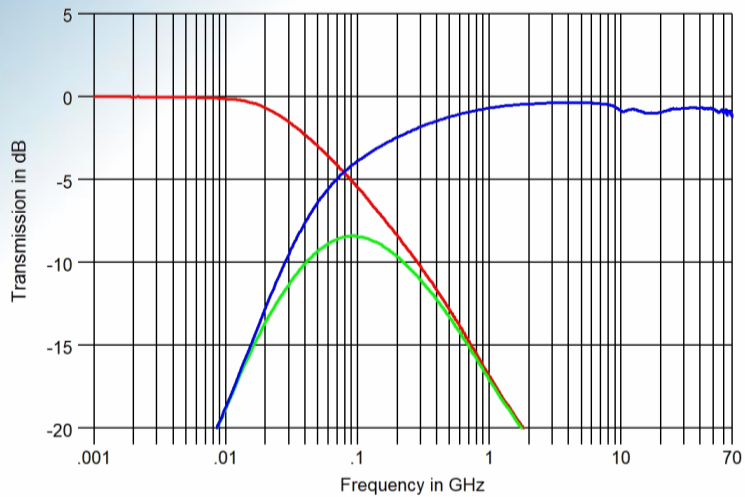
Parameter	Unit	Symbol	Min	Typ	Max	Conditions
<b>Absolute Maximum Ratings for SHF DX65R without Option and Option HV100</b>						
Maximum HF Input	dBm	$P_{in\ max}$			30	average power of a continuous <sup>1</sup> signal, 50 $\Omega$ load and $f \geq 2 \times f_{Low}$
Maximum Voltage w/o option	V				$\pm 20$	voltage across the coupling capacitor between HF and HF+LF port and between ports and GND
Maximum Voltage with Opt. HV100	V				$\pm 100$	voltage across the coupling capacitor between HF and HF+LF port and between ports and GND
Maximum LF Current	mA				$\pm 400$	
Case Temperature	$T_{case}$	$^{\circ}C$	10	25	50	
<b>Electrical Characteristics SHF DX65R without Option and Option HV100 (At 25<math>^{\circ}C</math> case temperature)</b>						
High Frequency 3 dB Point HF-Path	GHz	$f_{HIGH}$	65			reference is insertion loss at 0.5 GHz
Low Frequency 3 dB Point HF-Path	MHz	$f_{LOW}$			90	reference is insertion loss at 0.5 GHz
High Frequency 3 dB Point LF-Path	MHz	$f_{HIGH}$	25			reference is insertion loss at 0.5 GHz
Low Frequency 3 dB Point LF-Path	Hz	$f_{LOW}$			0	DC <sup>2</sup>
Insertion loss	dB	HF+LF/HF			2	>0.5 GHz <65 GHz
Reflection	dB	HF			-10	>0.5 GHz <65 GHz
	dB	HF+LF			-10	>0.5 GHz <65 GHz
	dB	LF			-10	<50 MHz
Group Delay Ripple	ps				$\pm 50$	2 GHz ... 65 GHz, 160 MHz Aperture
DC Resistance	$\Omega$			1		LF to HF+LF Port
<b>Mechanical Characteristics</b>						
Connector HF ; HF+LF LF	$\Omega$			50		1.85 mm SMA
Dimensions	mm					please see page 5
Weight	g			22		

<sup>1</sup> 30 dBm (1 W) equals 20 V peak to peak for continuous sinusoidal signals. A pulsed excitation with an average of 1 W and thus having significantly higher peaks may be possible. The maximum RF input power does not change in case a signal is applied to the LF port.

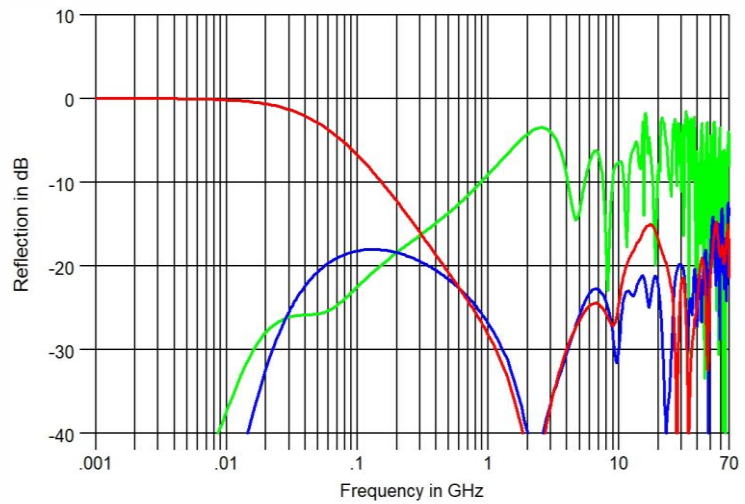
<sup>2</sup> For resonance-free transmission the LF port requires a 50  $\Omega$  termination.



# Typical S-Parameters for a DX65R

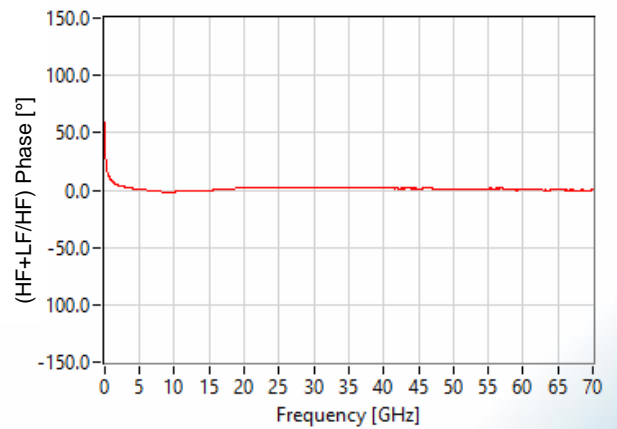
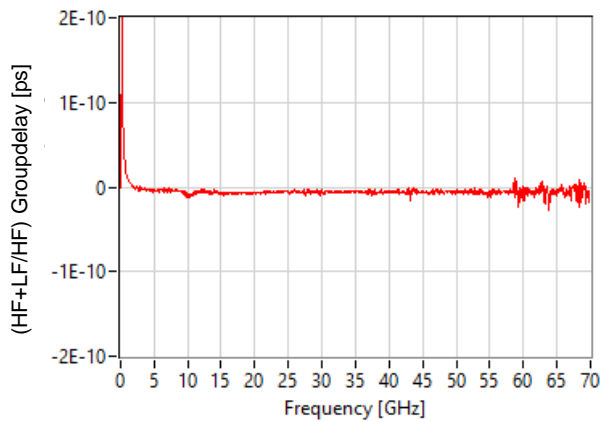


**HF+LF/HF: blue ; HF+LF/LF: red ; HF/LF: green**



**HF+LF: blue ; HF: red ; LF: green**

Please refer to the mechanical drawing for the pin assignment.

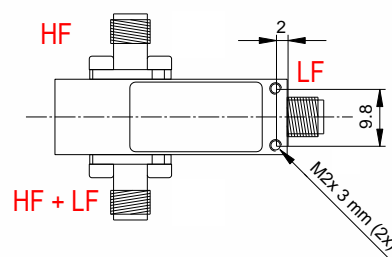
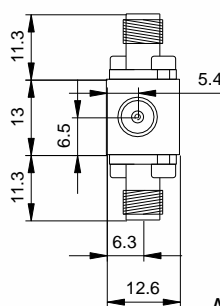
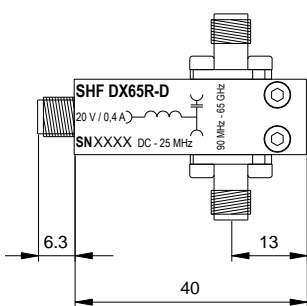
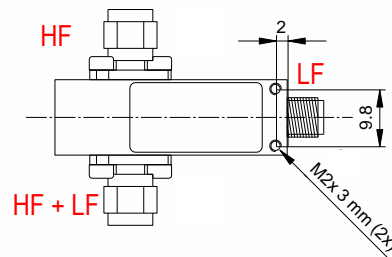
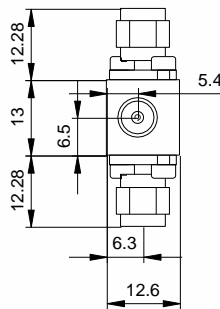
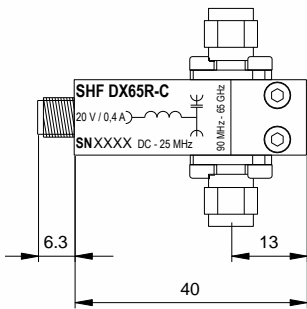
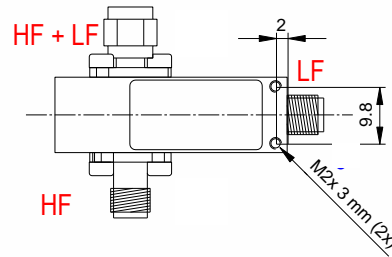
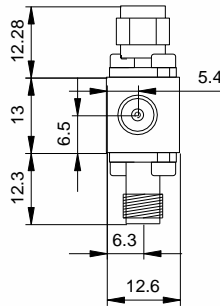
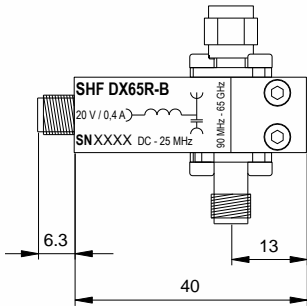
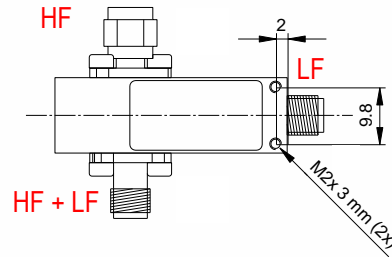
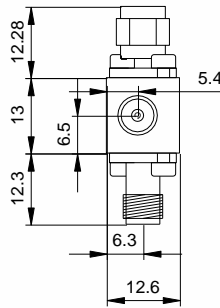
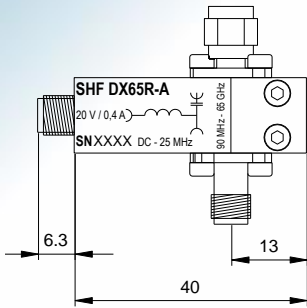
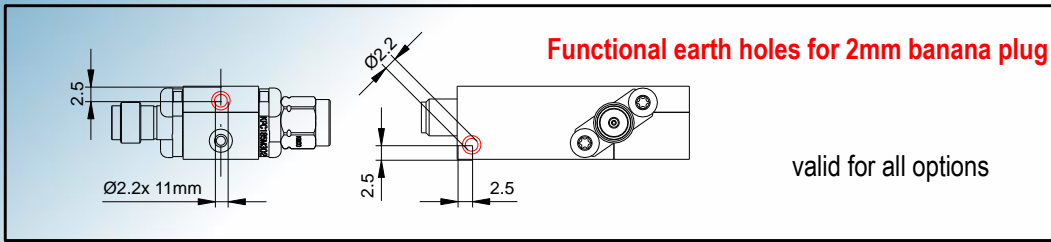


Aperture of group delay measurement: 160 MHz

Phase measurement has been compensated by propagation delay to visualize phase linearity.



# Mechanical Drawing



All dimensions in mm